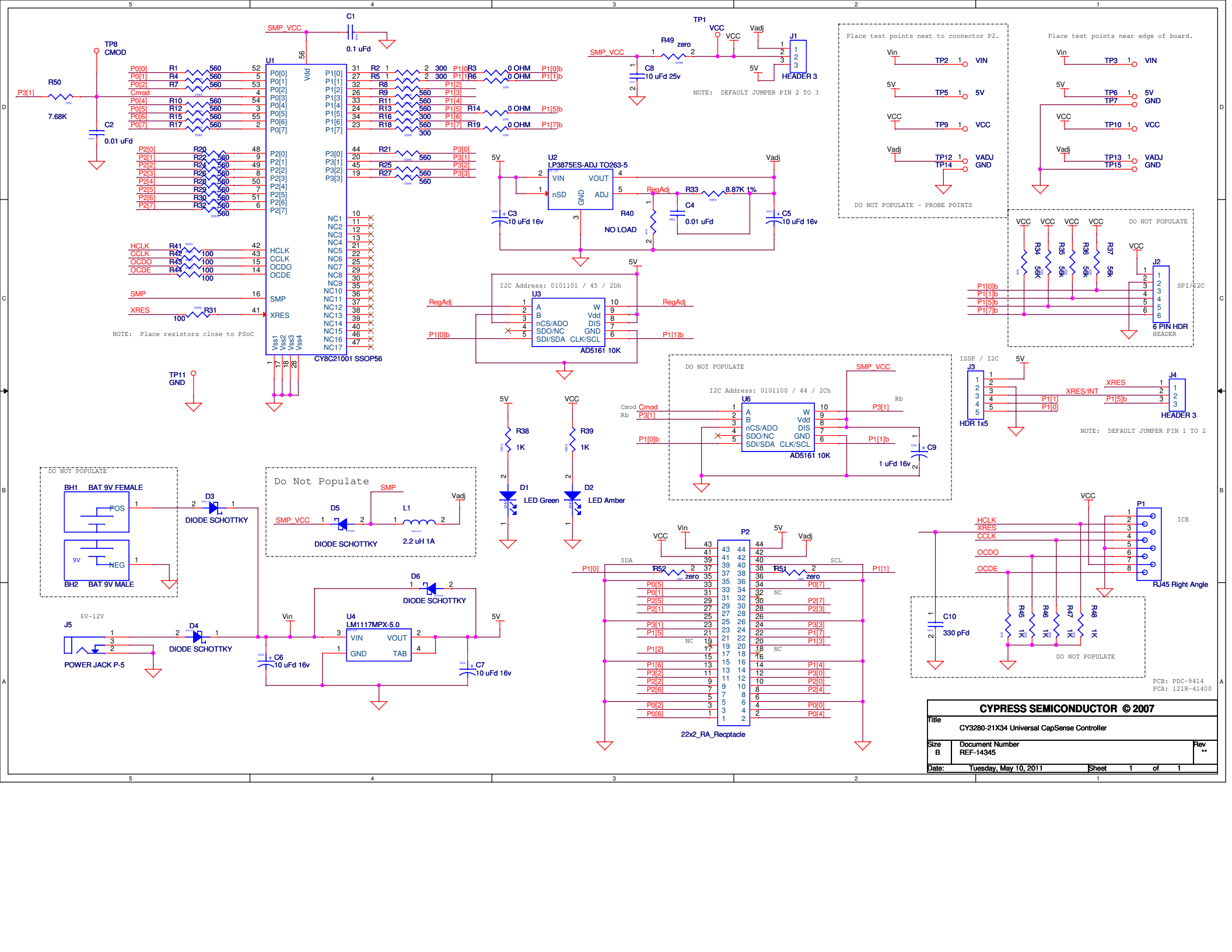

SMP_VCC
C1
0.1 uFd
TP8
CMOD
R50
7.68K
C2
0.01 uFd
U1
CY8C21001 SSOP56
NOTE: Place resistors close to PSoC
TP11
GND
TP1
VCC
Vadj
J1
HEADER 3
R49 zero
C8
10 uFd 25v
NOTE: DEFAULT JUMPER PIN 2 TO 3
Place test points next to connector P2.
TP2
VIN
TP5
5V
TP9
VCC
TP12
TP14
VADJ
GND
DO NOT POPULATE - PROBE POINTS
Place test points near edge of board.
TP3
TP6
TP7
TP10
TP13
TP15
U2
LP3875ES-ADJ TO263-5
C3
10 uFd 16v
R33
8.87K 1%
R40
NO LOAD
C4
0.01 uFd
C5
10 uFd 16v
DO NOT POPULATE
R34
R35
R36
R37
J2
6 PIN HDR
HEADER
SPI/I2C
I2C Address: 0101101 / 45 / 2Dh
U3
AD5161 10K
I2C Address: 0101100 / 44 / 2Ch
U6
AD5161 10K
C9
1 uFd 16v
ISSP / I2C
J3
HDR 1x5
J4
HEADER 3
XRES/INT
NOTE: DEFAULT JUMPER PIN 1 TO 2
R38
1K
R39
1K
D1
LED Green
D2
LED Amber
BH1
BAT 9V FEMALE
BH2
BAT 9V MALE
D3
DIODE SCHOTTKY
Do Not Populate
D5
L1
2.2 uH 1A
D6
J5
6V-12V
POWER JACK P-5
D4
U4
LM1117MPX-5.0
C6
10 uFd 16v
C7
10 uFd 16v
P2
22x2_RA_Recptacle
R52
R51
zero
P1
RJ45 Right Angle
ICE
C10
330 pFd
R45
R46
R47
R48
1K
PCB: PDC-9414
PCA: 121R-41400
CYPRESS SEMICONDUCTOR © 2007
Title
CY3280-21X34 Universal CapSense Controller
Size
B
Document Number
REF-14345
Rev
**
Date:
Tuesday, May 10, 2011
Sheet
1
of
1